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CONFIRMATION NO. 9488

SERIAL NUMBER 10/533,434	FILING OR 371(c) DATE 05/02/2005 RULE	CLASS 369	GROUP ART UNIT 2627	ATTORNEY DOCKET NO. 071971-0203
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APPLICANTS


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** CONTINUING DATA *****

This application is a 371 of PCT/JP04/08594 06/11/2004

** FOREIGN APPLICATIONS *****

JAPAN 2003-316774 09/09/2003
JAPAN 2003-407206 12/05/2003

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged	 Examiner's Signature	 Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 27	TOTAL CLAIMS 36	INDEPENDENT CLAIMS 1
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ADDRESS

20277

TITLE

Phase error detecting circuit and synchronization clock extraction circuit

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